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to attempt to program in VHDL as they would program a higher-level computer language. Higher-level computer languages are sequential in nature; VHDL is not. VHDL was invented to describe hardware and in fact VHDL is a con-current language. What this means is that, normally, VHDL instructions

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Zainalabedin Navabi, Ph.D., (Boston, MA) is adjunct professor of electrical and computer engineering at Northeastern University and the author of VHDL: Analysis and Modeling of Hardware (1993,...

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