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J. N T U W O R L D Code No: RR210202 JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD B.Tech II Year I Semester Examinations, May/June-2013 Pulse and Digital Circuits (Common to EEE, ECE) Time: 3 hours Max. Marks: 80 Answer any five questions All questions carry equal marks - - - 1.a) Prove that an RC low pass circuit behaves as a good integrator if  $RC \gg T$  where T is the Period of the ...

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Lecture1 - Introduction to Digital Circuits

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Pulse and Digital Circuits [Book]

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1 Pulse & Digital Circuits (PDC) - Introduction to syllabus JNTUH (R13)

Digital Signals • Digital waveforms change between the LOW and HIGH levels. A positive going pulse is one that goes from anormally LOW logic level to a HIGH level and then back again. Digital waveforms are made up of a series of pulses leadingedge (b)Negative–going pulse HIGH Fallingor Rising or trailingedge LOW (a) Positive–goingpulse ...

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