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level. In negative logic, a  
1 is represented by a  
LOW level, and a 0 by a  
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*Page 5/23*

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and a speedometer can

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have either an analog or a digital output. Section 1-2 Binary Digits, Logic Levels, and Digital Waveforms 4. In positive logic, a 1 is represented by a HIGH level and a 0 by a LOW level. In negative logic, a 1 is represented by a LOW level, and a 0 by a HIGH level.

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Digital Fundamentals 1  
Introduction to Digital  
Signal Processing.

Objectives

- List the essential elements in a digital signal processing system
- Explain how analog signals are converted to digital form
- Discuss the purpose of filtering
  - Describe the sampling process

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than analog data (2)  
Storing and reproducing  
of digital data is more  
accurate, compact and  
clear compared to  
analog data. (3) Digital  
data is not that affected  
by noise as analog data.

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