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JOHN F. WAKERLY WAKERLY THIRD EDITION DIGITAL DESIGN DIGITAL

EXERCISE SOLUTIONS DIGITAL CIRCUITS 3 3.1 The "probably" cases may cause damage to the gate if sustained. 3.3 A logic buffer is a non-linear amplifier that maps the entire set of possible analog input voltages into just two output votages, HIGH and LOW. An audio amplifier has a linear response over its specified operating range.

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