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RM0383 Reference manual - STMicroelectronics

† ARM CoreSight™ Components Technical Reference Manual (ARM DDI 0314) † ARM Debug Interface v5, Architecture Specification (ARM IHI 0031) † ARM Embedded Trace Macrocell Architecture Specification (ARM IHI 0014).

Technical Reference Manual

Hi, we are operating the M4F with 160MHz and we would like to know the MFLOPS we can achieve with this configuration. Regards, Timm

(PDF) Cortex-M4 Technical Reference Manual | Javier ...

For information on the Arm® Cortex®-M4 with FPU core, refer to the Cortex®-M4 with FPU Technical Reference Manual. Related documents ... refer to the STM32F3 Series, STM32F4 Series, STM32L4 Series and STM32L4+ Series Cortex®-M4 programming manual (PM0214). www.st.com. Contents RM0383 2/844 RM0383 Rev 3

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Programming manual STM32 Cortex®-M4 MCUs and MPUs programming manual Introduction This programming manual provides information for application and system-level software developers. It gives a full description of the STM32 Cortex®-M4 processor programming model, instruction set and core peripherals. The applicable products are listed in the table

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MFLOPS of M4F - Cortex-M / M-Profile forum - Processors ...

This manual is written to help system designers, system integrators, verification engineers, and software programmers who are implementing a System-on-Chip (SoC) device based on the Cortex-M3 processor.

CoreSight ETM -M4 - ARM architecture

• ARM®v7-M Architecture Reference Manual (ARM DDI 0403). • ARM® Cortex-M4 Integration and Implementation Manual (ARM DII 0239). • ARM® ETM-M4 Technical Reference Manual (ARM DDI 0440). • ARM® AMBA® 3 AHB-Lite Protocol (v1.0) (ARM IHI 0033). • ARM® AMBA™ 3 APB Protocol Specification (ARM IHI 0024).

ARM Cortex-M - Wikipedia

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ARM MCU Resources - Documentation - Processors blog ...

The ARM Cortex-M is a group of 32-bit RISC ARM processor cores licensed by Arm Holdings. They are intended for microcontroller use, and have been shipped in tens of billions of devices. The cores consist of the Cortex-M0, Cortex-M0+, Cortex-M1, Cortex-M3, Cortex-M4, Cortex-M7, Cortex-M23, Cortex-M33, Cortex-M35P.

Cortex M4 Technical Reference Manual

Table 3.1 shows the Cortex-M4 instructions and their cycle counts. The cycle counts are based on a system with zero wait states. The cycle counts are based on a system with zero wait states. Within the assembler syntax, depending on the operation, the <op2> field can be replaced with one of the following options:

PM0214 Programming manual

This book is for the CoreSight Embedded Trace Macrocell™ for the Cortex-M4 and Cortex-M4F processors, the CoreSight ETM-M4 macrocell. You implement the ETM-M4 macrocell with either the Cortex-M4 processor or the Cortex-M4F processor. In this manual, in general: † any reference to the processor applies to either the Cortex-M4 processor or the

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