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(CMOS), also known as  
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***(COS-MOS), is a type of  
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(metal-oxide-semiconductor  
field-effect transistor)  
fabrication process that uses  
complementary and  
symmetrical pairs of p-type  
and n-type MOSFETs for logic***

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used for constructing  
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***In order to derive maximum learning experience, the users are advised to first read the instructions for conducting the labs. There are 'step-by-step' instructions available in each lab to assist***

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## ***CMOS - Wikipedia***

***Very large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining millions of***

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***MOS transistors onto a single chip. VLSI began in the 1970s when MOS integrated circuit chips were widely adopted, enabling complex semiconductor and telecommunication technologies to be developed.***

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memory chips are VLSI  
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