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*In CMOS technology, both N-type and P-type transistors are used to
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delay. MOS stands for "Metal-Oxide-Semiconductor," and is a description of the top-to-bottom layers in a MOS chip.

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CMOS Technology Working Principle and Its Applications

Characteristics of TTL logic: Power dissipation is usually 10 mW per gate. Propagation delays are 10 nS when driving a 15 pF/400 ohm load. Voltage levels range from 0 to Vcc where Vcc is typically 4.75V - 5.25V. Voltage range 0V - 0.8V creates logic level 0. Voltage range 2V - Vcc creates logic level 1. CMOS compared to TTL:

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