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INTRODUCTION 1.1 4.7 2. 1.2 1.1 3. 1.3 4. ... Cmos digital integrated\_circuits Savvas Dimopoulos

Kang vlsi Shivam Singhal. Dynamic logic circuits Kalyan Kumar Kalita. Chapter 9 v6.0 ...

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time, power dissipation, and power density are affected in terms of the scaling factor,  $S$ . To be more specific, what would happen if the design rules change from, say,  $1 \mu\text{m}$  to  $1/S \mu\text{m}$  ( $S > 1$ )?

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