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Solution: (a) For the circuit in Fig. 1-23(a), the current entering the (+) terminal of the device is 0.2 A. Hence, the power P (where we use an uppercase letter because both the current and voltage are dc) is: $P = VI = 12 \times 0.2 = 2.4W$, and since $P > 0$, the device is a recipient of power. As we know, the law of conservation of power requires that if the

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It includes the NI myDAQ hardware and Circuits (Third Edition) by Fawwaz Ulaby, Michel Maharbiz and Cynthia Furse. Evaluation of myDAQ for Instructors: NI myDAQ Educator Evaluation Unit New problems, examples and enhancements throughout the presentation.

Homepage of Zheng Yang - UIC Engineering

Fawwaz T. Ulaby and Michel M. Maharbiz, Circuits c 2009 National Technology and Science Press Exercise 6.11 The circuit in Fig. E6.11 was already in steady state when the switch was opened at $t = 0$. Determine $v_C(0)$, $i_C(0)$, , 0 , and $v_C(t)$ for $t > 0$.

HW 4-4 & 4-5

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Circuits by Ulaby, Maharbiz, Furse

Fawwaz T. Ulaby, Michel M. Maharbiz and Cynthia M. Furse Circuits. c 2015 National Technology Press. Exercise 1-2 Convert the following quantities to a prefix format such that the number preceding the prefix. is between 1 and 999: (a) 8:32 107 Hz, (b) 1:67 10 8 m, (c) 9:79 10 16 g, (d) 4:48 1013V, (e) 762 bits/s.

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