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A Guide on Logical Equivalence Checking - Flow, Challenges ...

directive, and the directive does not affect the Conformal LEC software. Table 17-1 lists supported pragmas and trigger keywords for formal verification. c Do not use Verilog 2001-style pragma declarations. The Quartus II software and the Conformal LEC software support this style of pragma differently. Example 17-1.

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LEC comprises of three steps as shown below: Setup Mode, Mapping Mode and Compare Mode. Fig-1. Logical Equivalence Check flow diagram. There are various EDA tools for performing LEC, such as Synopsys Formality and Cadence Conformal. We are considering Conformal tool as a reference for the purpose of explaining the importance of LEC.

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LEC enables designers to verify the widest variety of circuits, including complex arithmetic logic, datapaths, memories, and custom logic. REQUEST INFORMATION . Already proven in thousands of tapeouts, Conformal EC is the industry's most widely supported independent equivalence checking product.

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Conformal LEC schematic Conformal LEC GUI debug tool gave some useful information, but not sufficient and efficient. The schematic from LEC debug tool printed out too many gates and connections. The culprit gates were hiding somewhere. It's intimidating to dive into this full-blown schematic and catch the culprit gate. Conformal LEC schematic ...

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Equivalence Checking Using Cadence Conformal LEC

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Conformal Smart LEC - Cadence Design Systems

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Conformal Smart Logic Equivalence Checker is the next-generation equivalency checking solution. With massively parallel architecture and adaptive proof technology, the Conformal Smart LEC delivers dramatic turnaround time improvements in equivalence checking by over 20X for RTL-to-gate comparisons.

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Cadence Conformal suite of tools contains a tool called Logic Equivalence Checker or LEC. Section 4 of this tutorial describes how to formally verify that the synthesized design is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use Formality for equivalence checking. During the ...

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